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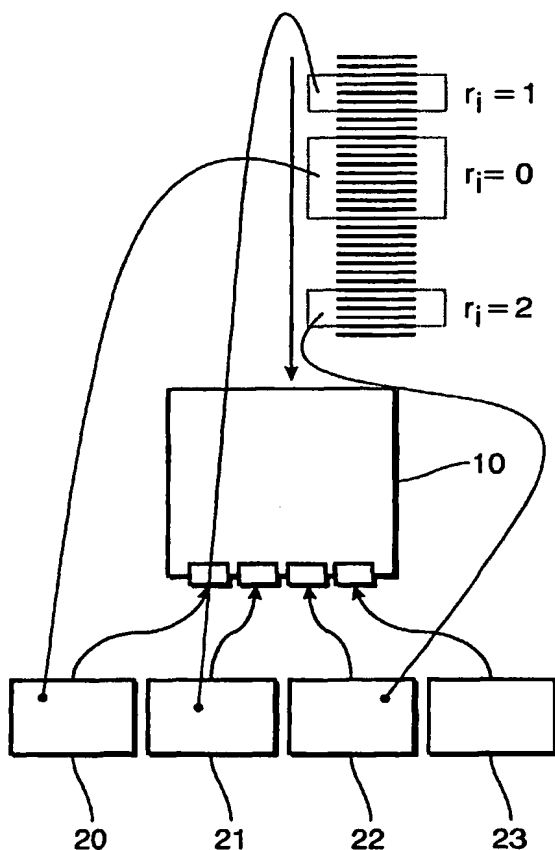
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- (75) Inventor/Applicant (for US only): **PESSOLANO, Francesco** [IT/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).
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(57) Abstract: The present invention relates to a processing method and apparatus for processing an information based on a sequence of instructions, wherein a repeated sub-sequence is detected in the sequence of instructions and an allocation between a processing resource and said repeated sub-sequence is determined based on an index information indicating the repetition frequency of the repeated sub-sequence. Thus, a combination of a scalable signal processor with automatic task distribution is provided, by means of which the number of memory accesses can be reduced, as the repeated sub-sequence can be allocated to external processing units, which are correspondingly programmed or which use their embedded memory. This also saves power.



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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
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| X | US 2002/029357 A1 (CHARNELL WILLIAM THOMAS ET AL) 7 March 2002 (2002-03-07) paragraph '0430! - paragraph '0431! --- | 1,12 |
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☒ Patent family members are listed in annex.

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